SPECIFICATION AMENDMENTS

Please change the Title to read:

SOLID-STATE IMAGE PICKUP APPARATUS HAVING A RESET TRANSISTOR CONTROLLED BY AN OUTPUT LINE

Please change page 4 of the Specification as shown in the marked-up version thereof which appears as the next-succeeding page.

selection output line 18-1 and a second row selection output line 18-2. A switch MOS transistor 19 supplies a pulse from the pulse terminal 14 to the transfer control line 10. A switch MOS transistor 20 supplies a pulse from the pulse terminal 15 to the reset control line 11. A switch MOS transistor 21 supplies a pulse from the pulse terminal 16 to the selection control line 12. The gates of the MOS transistors 19, 20, and 21 are connected to the row selection output line ~~18~~ 18-1. The state of the row selection output lines ~~18~~ 18-1 and 18-2, determines the row on which pixels become active.

The sensor also includes an readout circuit 22 for reading out an output from a pixel, a capacitor 23 for holding a reset signal output from a pixel, a capacitor 24 for holding a photo signal output from a pixel, a switch MOS transistor 25 for connecting/disconnecting the pixel output line 8 to/from the capacitor 24, a noise output line 27 to which the reset output held by the capacitor 23 is supplied, a signal output line 28 to which the optical output held by the capacitor 24 is supplied, a switch MOS transistor 29 for connecting/disconnecting the capacitor 23 to/from the noise output line 27, a switch MOS transistor 30 for connecting/disconnecting the capacitor 24 to/from the signal output line 28, a noise output line reset MOS transistor 31 for resetting the potential of the noise output line 27, a signal output line reset MOS